In-Network Computation is a Dumb Idea Whose Time Has Come

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Is this a dumb idea?

• increased complexity
• new kinds of failure modes to address
• could affect application correctness
• will put application-specific logic in the network...

But I didn’t wake up at 4AM to tell you something dumb
network programmability is the holy grail

I was already “programming the network” with network processors 13 years ago...
today’s context is very different

Tremendous requirements: reliability, velocity, performance

New HW breed: programmable ASICs, no power or cost penalties
as networks become capable of computation, what kinds of computation should networks perform?
Network machine architecture w/ PISA

**Match Logic**
(Mix of SRAM and TCAM for lookup tables, counters, meter, generic hash tables)

**Action Logic**
(ALUs for standard boolean and arithmetic operations, header modification operations, hashing operations, etc.)

Ingress match-action stages (pre-switching)

Egress match-action stages (post-switching)

Slide courtesy: Changhoon Kim
Why is compute in the network different than a general purpose CPU?
Temporal vs spatial compute

Slide idea from: Microsoft Build 2017: Inside the Microsoft FPGA-based configurable cloud
Inherent challenges

• Computing on this architecture corresponds to executing streaming algorithms with stringent constraints on number and type of ops

• Not Turing complete
• Limited memory size
• Limited set of actions (per stage & pipeline length)
• Few operations per packet (line rate execution guarantee)
Judicious in-network computing

• Bring high benefits at low costs
  • Is the network the bottleneck? Reduce bytes or reduce latency?

• Must live within the confines of HW constraints

• Application correctness is paramount

• Should give benefits in common case and possibly during failures too

• In the limit, should be no worse than if without

• Should focus on primitives that are broadly applicable to class of apps
  • Identify reusable, high-level abstractions that promote easy adoption
Tremendous opportunity

• Target important, widely used distributed services in the network

• Examples:
  
  • Fault-tolerance protocols
  
  • KV storage systems
  
  • Distributed ML training
ABSTRACT

The Paxos protocol is the foundation for building many fault-tolerant distributed systems and services. This paper posits that there are significant performance benefits to be gained by implementing Paxos logic in network devices. Until recently, the notion of a switch-based implementation of Paxos would be a daydream. However, new flexible hardware is on the horizon that will provide customizable packet processing pipelines needed to implement Paxos. While this new hardware is still not readily available, several vendors and consortia have made the programming languages that target these devices available. Notable examples include Huawei’s POF [28], Xilinx’s PX [3], and the P4 Consortium’s P4 [1]. Consequently, it is now possible for researchers to write programs that will soon be deployable on hardware, and run them in software emulators such as Mininet [4].

Furthermore, many academic researchers have now made the source code for their implementations publicly available with an extensive description of the algorithms and challenges involved. Notably, the P4Paxos project [1] has integrated the Paxos logic into a virtual switch running in a Mininet testbed.

In this paper, we describe an implementation of Paxos in a new syntax. It helps expose new practical concerns and design decisions for the algorithm that have not, to the best of our knowledge, been previously addressed. For example, a switch-based implementation of Paxos involves maintaining and consulting persistent state (including broadcast operations) in software, the same operations could instead, rather than executing server logic (including expensive message processing behavior, which would depend on a customized hardware implementation (and possibly coordination with a vendor).

Beyond these contributions, the exercise of implementing Paxos as Mininet [4] will provide a useful, concrete example of techniques that can be applied to other dataplane applications. All source code, as well as a short summary of the Paxos protocol, is publicly available under an open source license [5]. We hope this work will spark interest in making the code publicly available with an extensive description of the algorithms and challenges involved.

Finally, we discuss related work (§7) and future work (§8). We provide short summaries of the Paxos protocol (§2), an overview of available devices (§3), and the P4 language (§4). We then discuss our implementation in detail (§5). We also include a short discussion of optimizations (§6) and challenges (§8) in the implementation.

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REFERENCES


SIGCOMM CCR 2016
Example: NetPaxos

Consensus is a fundamental problem for fault-tolerant systems

- Offering consensus as a network service has significant performance benefits
- Implement Paxos logic in network devices
- Demonstrate consensus at 9 M msgs / s (4.3x improvement) and low latency (80% reduction)

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<thead>
<tr>
<th></th>
<th>P4FPGA</th>
<th>SDNet</th>
<th>Netronome</th>
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In-Network Computation is a Dumb Idea Whose Time Has Come

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ABSTRACT
Programmable data plane hardware creates new opportunities for infusing intelligence into the network. This raises a fundamental question: what kinds of computation should be delegated to the network?

In this paper, we discuss the opportunities and challenges for co-designing data center distributed systems with their network layer. We believe that the time has finally come for offloading part of their computation to execute in-network. However, in-network computation tasks must be judiciously crafted to match the limitations of the network machine architecture of programmable devices. With the help of our experiments on machine learning and graph analytics workloads, Programmable networks create the opportunity for in-network computation, i.e., offloading a set of compute operations from end hosts into network devices such as switches and smart NICs. In-network computation can offer substantial performance benefits, as it is for example the case with consensus protocols [9, 10] and in-network caches [20]. Although traditional networks are not capable of computation, the idea of using the network not just to move data, but also to perform computation on transmitted data is reminiscent of Active Networks [30], which proposed to replace packets with small programs called “capsules” that are executed at each traversed switch. However, for the past two decades the hardware capabilities were lacking. This appears to be
Example: DAIET  
https://sands.kaust.edu.sa/daiet/

Data aggregation is a common task in many DC apps; high potential for ML

- Offload aggregation task to switches to alleviate communication bottlenecks and improve overall training time
- Exploit full network bandwidth of workers

Aggregation micro-benchmark:
- 1.28GB, 320M-element tensor
- Tofino switch
- 2 to 8 workers at 10Gbps

Results:
**Transfer time 1.9 s** (1.56s optimal limit)
Time has come for in-network computing
The ball is in your court

• Some other ideas:
  • Congestion control, resource allocation (HotCoCoA, Sweden, Sharma et al. NSDI ’17)
  • Network measurements (Marple, Sonata, Dapper, INT)
  • Load balancing (SilkRoad)
  • Consensus protocols (NetPaxos, NoPaxos, Eris, NetChain)
  • Caching (IncBricks, NetCache)
  • Stream processing (Linear Road)

With great powers come great responsibilities
Use in-network computing judiciously!